## **ABSTRACT**

A channel estimation circuit (101) of the present invention includes: a tentative channel estimation unit (102) for performing channel estimation by making use of a received signal and outputting the result of estimation as a tentative channel estimation signal; a noise/interference power estimation unit (103) for estimating noise and interference power by making use of the received signal and outputting the result of estimation as a noise/interference power estimation signal; a threshold decision unit (104) for establishing a threshold signal by making use of the noise/interference power estimation signal to output the threshold signal; and an effective path detection unit (105) for outputting, as a channel estimation signal, paths of the tentative channel estimation signal that remains after noise paths having powers smaller than the threshold signal have been removed.

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